

PQV product Development Engineer

Responsible for ensuring the testability and manufacturability of integrated circuits from the component feasibility stage through production ramp. Make significant contributions to design, development and validation of testability circuits. Evaluation, development and debug of complex test methods. Develops and debugs complex software programs to convert design validation vectors and drive complex test equipment. Creates and tests validation and production test hardware solutions. Tests, validates, modifies and re-designs circuits to guarantee component margin to specification. Analyzes and evaluates component specification versus performance to ensure optimal match of component requirements with production equipment capability with specific emphasis on yield analysis and bin split capability. Analyzes early customer returns with emphasis on driving test hole closure activities. Creates and applies concepts for optimizing component production relative to both quality and cost constraints. Autonomously plans and schedules own daily tasks, develops solutions to problems utilizing formal education and judgment.

Qualifications:

Applicant's skills should ideally include some of the following: 1 Design for Test and Design for Debug usage experience in SCAN design/verification/validation experience preferred 2 IP based arch concepts are desirable 3 Silicon debug and characterization of new products to identify and close silicon issues. 4 Tester Experience to support debug and characterization of silicon ability to define and execute experiments is preferred 5 Strong spoken and written communication skills 6 Strong problem solving and prioritization ability 7 Independent and Inquisitive

1. HVE Product Development Engineer

In this position, the candidate will be trained to have a comprehensive grasp of the overall Manufacturing flow for HVM, operating Class ATE testers, setting up on NPI tools, and supporting test program development teams in debug & validations. The selected candidate will be involved in product engineering activities such as dealing with issues of cost, productivity, quality, and performance, reliability, and customer returns. It will also include developing, designing, or checking out test hardware, qualifying new products and performing detailed test program analysis to ensure successful product startup. Candidate will need to autonomously plan and schedule own daily tasks, develops solutions to problems utilizing formal education and judgment.

Qualifications:

Basic qualification - Master's Degree and/or Bachelor's Degree in Electronics, Electrical, Microelectronics, Science or a related technical degree. General knowledge, personal skill, and working experience to be considered as hiring advantage: Working experience in semiconductor industrial, possess semiconductor HVM manufacturing process flow knowledge Possess statistical data analysis and data system automation skills Possess strong teamwork, communication, problem-solving and influencing skills Fun and cheerful personality with strong learning desires

Analog Product Development Engineer

Responsible for deploying collateral and readiness for testability and manufacturability of IC integrated circuit from development stage into HVM High Volume Manufacturing factory and ready for production ramp PRQ. Drives HVM product IC meet or exceed PHI Product Health Indicator to meet volume ramp requirement. Monitor HVM product manufacturing efficiency is consistently meeting preset goal. In case of deficit, perform first cut issue debug/analysis including commonality analysis, validating production test hardware/software, etc, and summarize issue for escalating to rightful/ultimate owner for solution. Track solution development progress according to committed deadline and own solution deployment to HVM operation. Responsible for continuous manufacturing efficiency trend monitoring, identify improvement opportunity with first cut ROI study before develop and deploy the improvement proposal, or submitting the improvement project to rightful/ultimate owner. Responsible for the optimization of HVM product production relative to both quality and cost constraints. Owner to analyze FACR/customer returns by performing electrical Go-No-Go analysis using HVM ATE Class and xPV testers, own reporting the ATE analysis result to PQE/CQE for follow-up response to customer Plan and schedule daily tasks, develop solutions to problems utilizing formal education and judgment based on standard practices.

Qualifications:

Basic qualification - Master's Degree and/or Bachelor's Degree in Electronics, Electrical, Microelectronics, Science or a related technical degree
General knowledge, personal skill, and working experience to be considered as hiring advantage:
Working experience in semiconductor industrial, possess semiconductor HVM manufacturing process flow knowledge
Possess statistical data analysis and data system automation skills
Possess strong teamwork, communication, problem-solving and influencing skills
Fun and cheerful personality with strong learning desires

Sort Class test Product Development Engineer

Responsible for deploying Sort Class Test Program collateral and readiness for testability and manufacturability of IC integrated circuit from development stage into HVM High Volume Manufacturing factory and ready for production ramp PRQ. * Execute and build test case study for manufacturing testability and study.* Drive ATE Test Program platform programming.* Responsible for continuous manufacturing efficiency trend monitoring, identify improvement opportunity with first cut ROI study before develop and deploy the improvement proposal, or submitting the improvement project to rightful* Plan and schedule daily tasks, develop solutions to problems utilizing formal education and judgment based on standard practices.

Qualifications:

Basic qualification - Bachelor's Degree in Electronics, Electrical, Microelectronics, Science or a related technical degree * General knowledge, personal skill, and working experience to be considered as hiring advantage: * Working experience in semiconductor industrial, possess semiconductor HVM manufacturing process flow knowledge where applicable* Possess

statistical data analysis and data system automation skills * Possess strong teamwork, communication, problem-solving and influencing skills * Fun and cheerful personality with strong learning desires

Pre-Si emulation-FPGA solution Graduate Trainee

- This position is to be part of the Malaysia Design Center MDC under Pre-Silicon Solution and Capabilities PSC group within the Manufacturing & Validation Engineering MVE organization in Penang. Responsibility includes developing capabilities on pre-silicon environment in Virtual Platform/Emulation/FPGA, model build and compilation, validation test development and platform debug. The candidate will be working on Intel SoC silicon development, involve in front-end and back-end compilation to build pre-si model virtual platform/emulation/FPGA for system validation. This required broad understanding of multiple system architecture, and protocol including PCIe, USB, LPDDR, UFS, eMMC, SD, I2C, SPI and etc.

Qualifications:

Bachelors/Masters in Computer Science/Electrical/Electronic Engineering. Knowledge in C/C++, System Verilog, Python, IA, SoC Architecture, UPF and Low Power Flows. Experience in debug, RTL validation, SW validation, Power/Perf validation or manufacturing validation is a plus. Experience in Emulation, Virtual Platform, Altera/Xilinx/Synopsys/Mentor/Cadence Tools Flow, HAPS is a plus

System On Chip/SOC Silicon Design Graduate Trainee

Job Description:

Job Description: In this position, you will be involving in the training, design and development of next generation SOC/CPU for wide range of Intel products ranging from Client PC , smartphone, tablet to wearable. Your responsibilities will include some of the following but not limited to: -

- Assist design unit owner in Register Transfer Level RTL model functional validation. Use CAD tool extensively to simulate logic behavior and circuit performance and direction of physical design for next generation, deep sub-micron embedded circuit solutions. Verify the circuit behavior against the original simulation model and first silicon.
- Define VLSI Structural Design methodology and developing design flows. Implement structural physical designs, such as synthesis, floor planning, power-grid and clock tree designs, timing budgeting and closure, place and route, RC-extraction and integration. Verify structural physical designs, such as functional equivalency, timing/performance, noise, layout design rules, reliability and power.
- Develop Analog IP on next generation deep submicron process for the Intel's SOC, perform tasks related to Very-large-scale integration VLSI complementary metal-oxide-semiconductor CMOS IC design, Solid state physics and physical layout. Such tasks may include: Circuit design of high speed clocking related circuits [phase-locked loop PLL, delay-locked loop DLL, bandgap]

or high voltage input/output IO [double data rate DDR/LPDDR, General-purpose input/output GPIO, OPIO].

- Responsible for Integration of Third party IPs -- Synthesis, functional and/or timing convergence, and pre and post-si debug of IPs developed by various external vendors as well as within the company. Handling of signals crossing power planes and clock domains, industry standard protocols including hardware and software details dealing with Memory LPDDR, storage eMMC, SATA, UFS, peripherals PCIe, USB, and MIPI interfaces in SOC devices. System integration dealing with Si/ Platform/ FW/ MW/ drivers/ OS/ Apps on Android Windows-based tablets and phones.

Qualifications:

You must possess a Bachelor of Engineering degree or a Master of Science degree in Electronic, Electrical or Computer Engineering. Additional qualifications include: -

- Familiarity with Very Large Scale Integration VLSI Complementary Metal-Oxide Semiconductor CMOS logic circuit design.
- Well versed in UNIX*, C programming and relevant Computer Aided Design CAD tools.

Platform Application Engineer

Job Description:

As a Graduate Trainee of the IOTG Customer Engineering ICE Software Platform Application Engineering team, you will provide Software support to IOTG customers by replicating and debugging issues reported by our customers. You will have the hands-on opportunity to learn and experiment with the latest Intel platforms and software. You must be able to develop relationships and work cross-functionally with other team member.-Replicate reported issues on our customer reference boards.-Debug and root cause issues where applicable.-Create best known configurations for a particular platform/ OS.-Provide technical feedback to Intel Product Team to enhance upcoming Intel platform, technologies and platforms.-Create and update relevant software collateral.

Qualifications:

Must possess:-BS or MS degree in Computer Science, Computer Engineering, Electrical Engineering or equivalent-Experience in C/ C++ or scripting languages Perl, Ruby etc.-Strong communication skills in both written and verbal-Excellent problem solving skills-Willing to learn attitudeNice to have:-Knowledge of Intel Architecture and chipsets-Knowledge of Windows and Linux installation and setup.-Knowledge of Android installation.

Product Development Solutions Graduate Trainee

Job Description:

Come join Intel's Product Development Solutions PDS organization as a Design Automation Engineer/CAD Software Engineer. In this role you will be shaping the future of computing. We bring innovative technologies to the most complex microprocessor and SoC products on the planet. In this role, you will be a part of developing new and creative methods to increase efficiency and reduce time to market for these designs, helping to increase Intel's lead in computing. You will combine knowledge of logic and circuit design with software development techniques to deliver cutting edge solutions, shaping the way these products come to life. You will work with the best engineering talent in the world to collaborate on these new methods. You will play a pivotal role in our mission to revolutionize computing. Your responsibilities will include but not be limited to: - Define and develop methodologies/processes to enable better utilization of CAD tools to improve design efficiency. - Interact closely with Hard-IP and SoC design teams to mature the CAD solutions for production use. Activities include problem analysis and requirements definition, followed by design, implementation, test, deployment, and technical support of the flows. Deploying software tools to the design team, creating and delivering effective documentation and training, and providing support. - Automating design tasks in order to complete the product design in the most efficient fashion possible. - Developing tools and solutions to meet Intel's specific, rapidly evolving requirements for designing, manufacturing and testing of Intel products, by utilizing formal education and judgment. You are expected to perform complex product design, systems analysis and programming activities on applications software which may require some research. The ideal candidate should exhibit the following behavioral traits: - Problem-solving skills - Strong analytical skills - Ability to multitask - Strong written and verbal communication skills - Ability to work in a dynamic and team oriented environment

Qualifications:

- You must possess a minimum of Bachelors Degree in Computer Science, Computer Engineering or Electrical Engineering Masters Degree in Computer Science, Computer Engineering or Electrical Engineering is preferred. Additional/Desired Qualifications: - 2 years experience in one or more of VLSI design, physical design, logic design, static timing, power analysis, and optimization - 2 years experience with software development as part of a larger system, including proficiency in C/C++ - 2 years experience with scripting languages such as Perl and TCL and/or TK